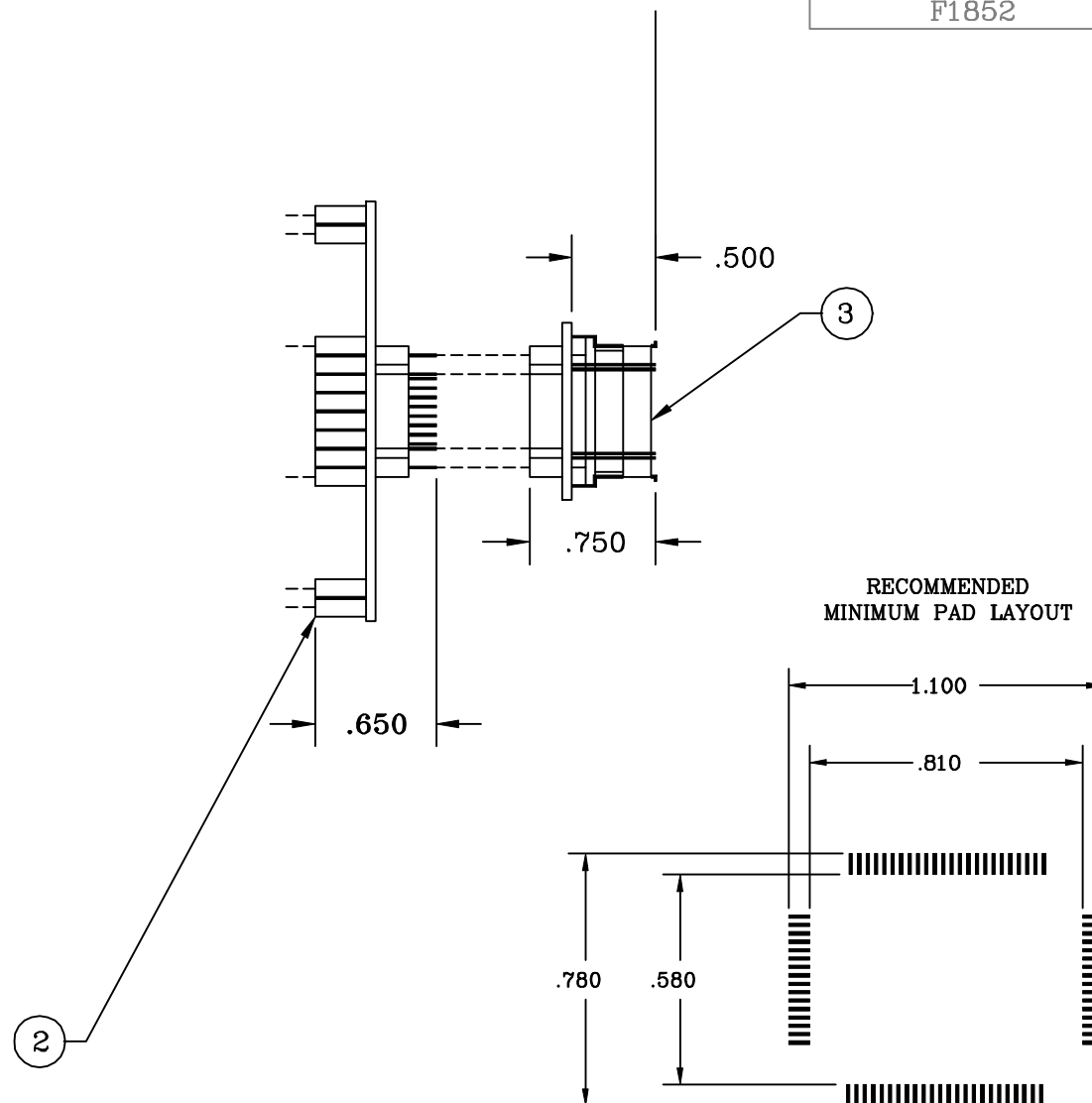




NO.	ITEM	DESCRIPTION	QTY.
-	---	---	---
2	MX-080-QF08-SUB	MIDDLE SUB ASSEMBLY	1
3	EPP-080-QF08	BOTTOM SUB ASSEMBLY	1
4	S-PCC-11-068-A	PLUG ON TOP OF #1	1



Emulation Technology, Inc.

— VLSI and SMT ADAPTERS and ACCESSORIES —

2344 Walsh Avenue, Bldg. F
Santa Clara, Ca 95051

TEL:(408)982-0660
FAX:(408)982-0664

SHEET:
1 OF 1

DATE:
2-10-93

REVISION:

ASSEMBLY DRAWING

CHECKED:
GABOR KARDOS

DRAWN:
SCOTT BARCELLOS

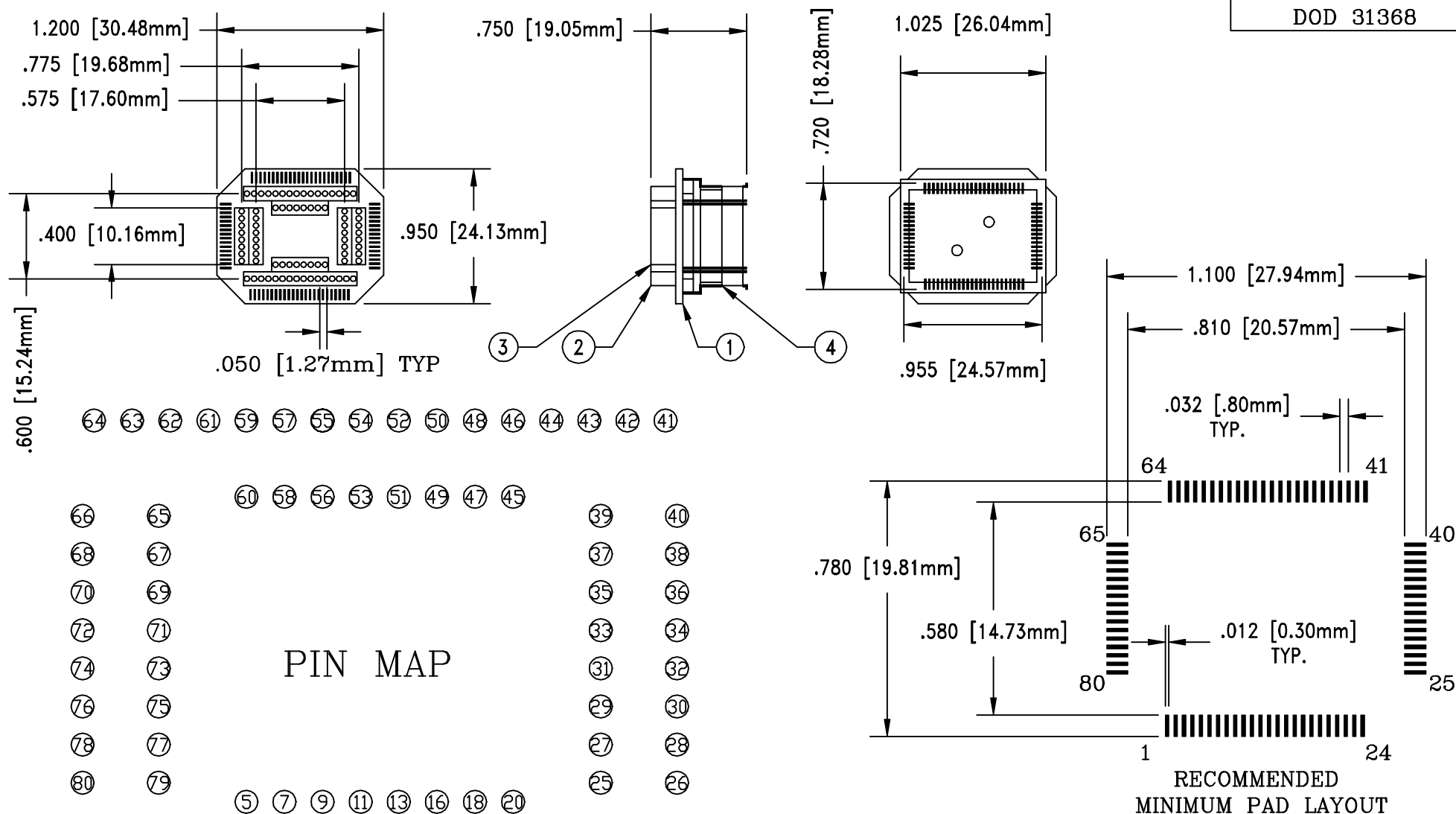
ITEM:
AS-V-088S

DESCRIPTION:
AS-PCC1-QF08S-V40

DO NOT SCALE DRAWING

F1368

DOD 31368



PIN MAP

RECOMMENDED
MINIMUM PAD LAYOUT

NO.	ITEM	DESCRIPTION	QTY.
1	80-QF08-INTEL	BASE BOARD	1
2	T16-FEM-50MIL	MOUNT ON FRONT OF #1	2
3	T09-FEM-50MIL	MOUNT ON FRONT OF #1	6
4	QF08-SPACERF-A	MOUNT ON REAR OF #1	1



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Santa Clara, Ca 95051TEL:(408)982-0660
FAX:(408)982-0664SHEET:
1 OF 1DATE:
10-30-00

REVISION:

ASSEMBLY DRAWING

CHECKED:
Paula OlsonDRAWN:
Miguel Delgadillo

ITEM: EPP080QF08SM

DO NOT SCALE DRAWING

DESCRIPTION:
EPP-080-QF08-SM